

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (currently amended): In a control circuit of the type comprising ~~a power supply~~, a shunt resistor and a controllable high-speed solid state switch device mounted on a circuit board for connecting ~~a~~ the power supply to a load device:

a detector mounted on the circuit board and having inputs connected across the shunt resistor and an output connected to control the state of the switch device;

the circuit board ~~said shunt resistor comprising an insulative support~~ having parallel opposite faces, and the shunt resistor having first and second electrically continuous conductor traces disposed in overlying relationship on said opposite faces.

Claim 2 (original): A circuit as defined in claim 1 wherein the detector is a comparator.

Claim 3 (original): A circuit as defined in claim 1 wherein the detector is an electronic amplifier.

Claim 4 (original): A circuit as defined in claim 3 further including a microcontroller having an output connected to the switch device and an input connected to receive the output of the electronic amplifier;

the microcontroller being operative to control the state of the switch device according to a signal developed across the shunt resistor.

Claim 5 (original): A circuit as defined in claim 1 wherein the first and second traces have multiple legs on each of said faces.

Claim 6 (original): A circuit as defined in claim 1 wherein the shunt resistor comprises third and fourth traces electrically continuous with the first and second traces.

Claim 7 (currently amended): A fast response control circuit comprising:

an electronic switch device having an output circuit and an input circuit, a milliohmic shunt resistor connected in series with the output circuit;

a detector having an input circuit connected across the shunt resistor and an output which changes state according to changes in voltages developed across the shunt resistor;

means connected to the output of the detector to control the switch device;

said shunt resistor comprising a multi-layered dielectric support with electrically continuous first and second conductive traces printed in overlying parallel relationship on opposite faces of the layers thereof and a via electrically connected to a bottom trace and extending back through the support, the input circuit of the detector being connected across a top trace and the via and electrically interconnecting the traces at one end thereof such that load current flows in opposite directions through the first and second traces.

Claim 8 (original): An automotive accessory control circuit comprising an FET switch and a shunt resistor connected in series between a power supply and a drive motor;

a detector having inputs connected across the shunt resistor and an output which changes state in accordance with voltages developed across the shunt resistor;

a microcontroller having an input connected to receive the output of the detector and being programmed to provide an output which changes state in accordance with a change in state of the detector output; and

a FET driver connected to receive the output of the microcontroller for controlling the state of the FET switch;

wherein said shunt resistor comprises a dielectric support with first and second conductive traces printed in overlying parallel relationship on opposite faces thereof and an electrical connector extending through the support and electrically connecting the traces at one end thereof.

Claim 9 (original): A circuit as defined in claim 8 wherein the first and second traces have multiple legs on each of said faces.

Claim 10 (original): A circuit as defined in claim 8 wherein shunt resistor comprises third and forth traces electrically continuous with the first and second traces.

Claim 11 (original): A circuit as defined in claim 8 wherein the detector is an amplifier.

Claim 12 (original): A circuit as defined in claim 8 wherein the detector is a comparator.